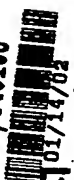


JC879 U.S. PTO
10/043180



PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10043180	FILING DATE 01/14/2002	CLASS 216-	SUBCLASS 2	GAU 1746	EXAMINER
**APPLICANTS: Kofuji Naoyuki; Mori Masahito; Itabashi Naoshi; Tsutsumi Takashi;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-281001 09/17/2001 JAPAN 2001-068030 03/12/2001					
PG-PUB <input type="checkbox"/> DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>			
Foreign priority claimed <input type="checkbox"/> yes <input type="checkbox"/> no		35 USC 119 conditions met <input type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
Verified and Acknowledged Examiners's initials				XA-9608	
TITLE : Method for manufacturing a semiconductor device					

U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G.
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg.
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner	
		PREPARED FOR ISSUE	
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